	Application No.	Applicant(s)	
	40/005 005	LEE HALINIAN	CAC
Notice of Allowability Ex	10/605,325 Examiner	LEE, JIAHN-LIN Art Unit	· · · · · · · · · · · · · · · · · · ·
	EXMITTION	721 0	
	Tai Duong	2871	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate committee GHTS. This application is	in this application. If not included nunication will be mailed in due co	ourse. THIS
1. This communication is responsive to <u>Amendment 11/24/05</u>	į.		
2. 🛮 The allowed claim(s) is/are <u>1,3,4,8 and 28</u> .			
 3. Acknowledgment is made of a claim for foreign priority uners. a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 		or (f).	
□ Certified copies of the priority documents have □ Certified copies of the priority documents have		on No	
3. ☐ Copies of the certified copies of the priority documents have	• •		on from the
International Bureau (PCT Rule 17.2(a)).	Cuments have been receive	ed iii diis nadonal stage applicade	ii nom ale
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	ENT of this application. itted. Note the attached EX	AMINER'S AMENDMENT or NO	
INFORMAL PATENT APPLICATION (PTO-152) which give	• • •	or declaration is deficient.	
5. CORRECTED DRAWINGS (as "replacement sheets") mus			
(a) including changes required by the Notice of Draftspers		w (PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the			ack) of
 DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT 	SIT OF BIOLOGICAL MAT FOR THE DEPOSIT OF BI	ERIAL must be submitted. No OLOGICAL MATERIAL.	te the
Attachment(s)	E 🗆 Notice of I	oformal Datast Application (DTO	150)
 Notice of References Cited (PTO-892) D Notice of Draftperson's Patent Drawing Review (PTO-948) 	_	nformal Patent Application (PTO- Summary (PTO-413),	152)
		./Mail Date s Amendment/Comment	
 Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 	8), 7. ∐ Examiner's	s Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	_	s Statement of Reasons for Allow	ance
	9. 🗌 Other	_ •	

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Amended claim 1 is allowed over the prior art of record because none of the prior art discloses or suggests a liquid crystal display comprising an enclosed wall structure, provided in between two substrates, wherein the enclosed wall structure and the two substrates form a first enclosed space, a sealant, formed outside said enclosed wall structure between said two substrates, wherein said sealant and said two substrates form a second enclosed space, and at least a thin film transistor, being formed in said first enclosed space on one of said two substrates, wherein the enclosed wall structure comprises a conductive wall. Claims 3, 8 and 28 are also allowed since they depend on allowed claim 1.

Amended claim 5 is allowed over the prior art of record because none of the prior art discloses or suggests a liquid crystal display comprising an enclosed wall structure, provided in between two substrates, wherein the enclosed wall structure and the two substrates form a first enclosed space, a sealant, formed outside said enclosed wall structure between said two substrates, wherein said sealant and said two substrates form a second enclosed space, and at least a thin film transistor, being formed in said first enclosed space on one of said two substrates, wherein a plurality of conductive walls for conducting said two substrates are provided on said substrate that has said thin film transistors, wherein the conductive walls are located within the first enclosed space.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication should be directed to Tai Duong at telephone number (571) 272-2291.

The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

DUNG T. NGUYEN PRIMARY EXAMINER

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IVD

12/05